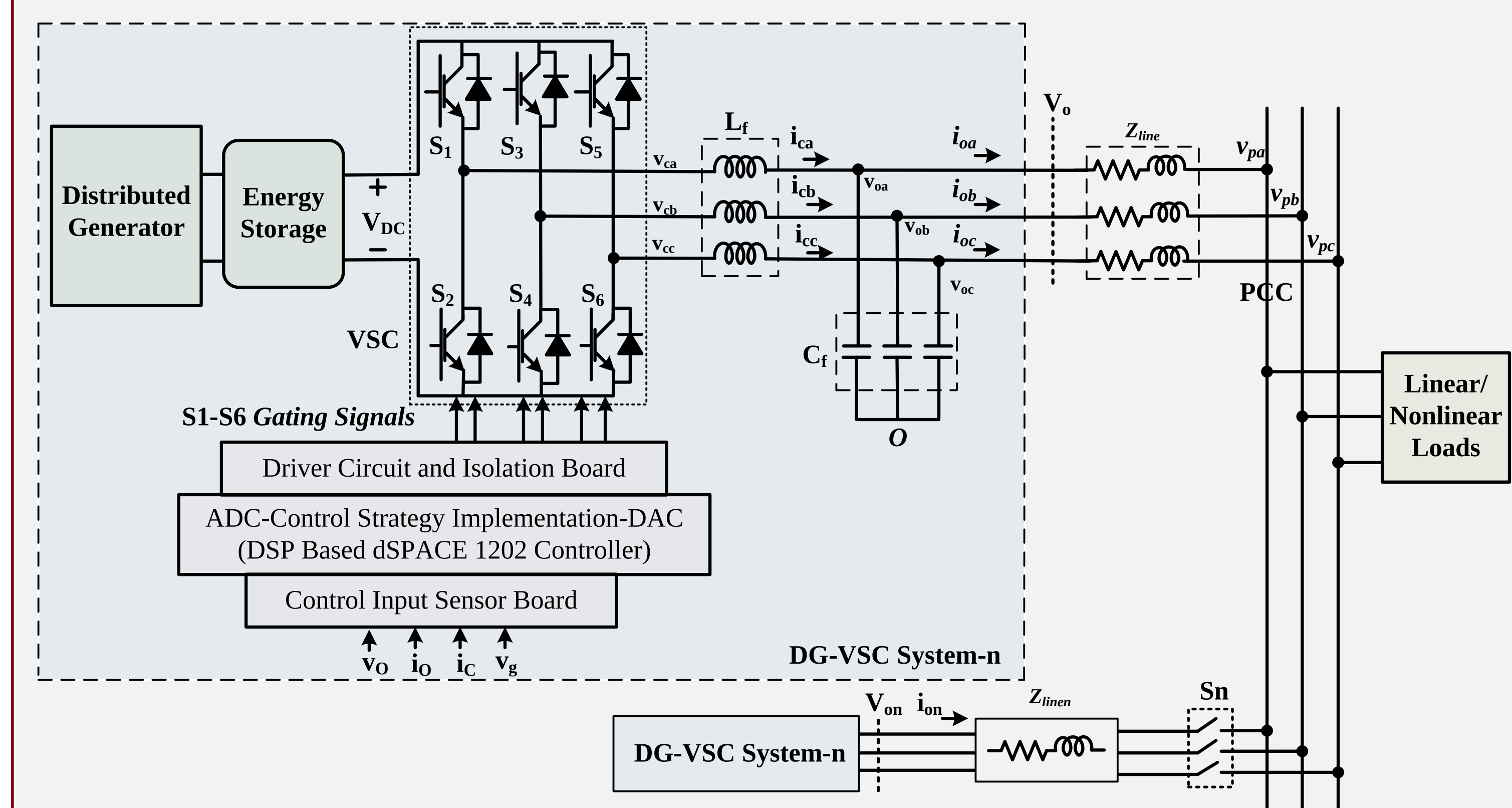


Predictive Optimal Switching Vector Controlled Inverters for AC Microgrids- with Overcurrent/ Switching Frequency Constraints

V L Srinivas (2015EEZ2511)
Bhim Singh* and Sukumar Mishra*

ABSTRACT: The conventional pulse width modulation controllers use output voltage and current with inner and outer current loops, thereby demanding the PI regulators and PWM modulators. They are associated with finite dynamic response transient time by PI controllers and necessitate rigorous tuning for practical implementation purposes. To complement this problem, voltage and current predictive optimal switching vector control is proposed here, which eliminates the PI regulators, PWM modulators and synchronous co-ordinate transformations. It uses the model of the system to predict behavior of output voltages for each sampling instant.

SCHEMATIC DIAGRAM



CONTROL STRUCTURE

A. System Modeling: $\mathbf{x}(k+1) = \mathbf{A}_x \mathbf{x}(k) + \mathbf{B}_x \bar{\mathbf{v}}_c(k) + \mathbf{B}_x \bar{\mathbf{v}}_o(k)$; $\mathbf{A}_x = e^{\mathbf{A}T_s}$, $\mathbf{B}_x = \int_0^{T_s} e^{\mathbf{A}\tau} \mathbf{B}_x d\tau$, $\mathbf{B}_v = \int_0^{T_s} e^{\mathbf{A}\tau} \mathbf{B}_v d\tau$

B. Predictive Optimal Switching Vector Controller:

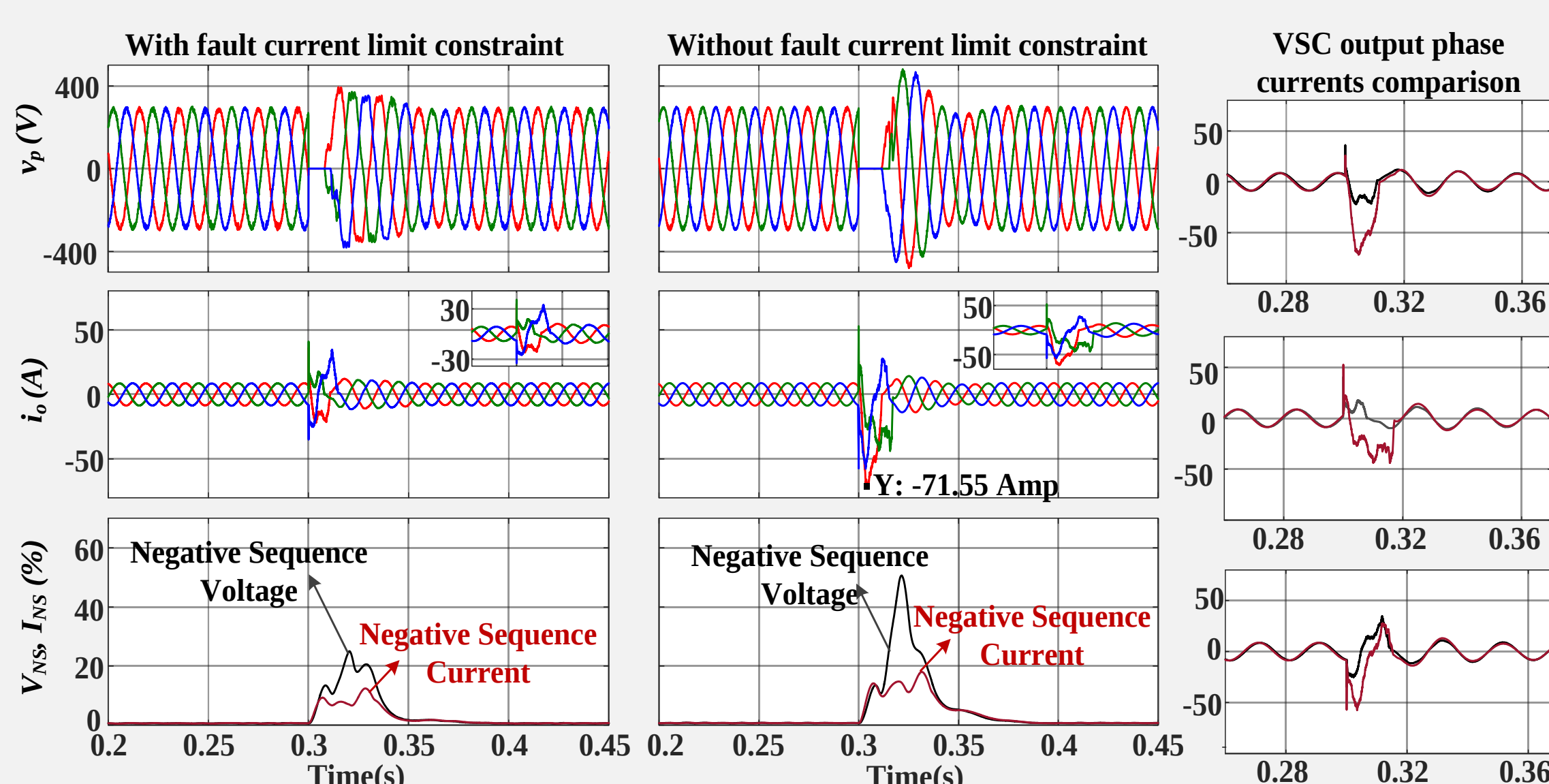
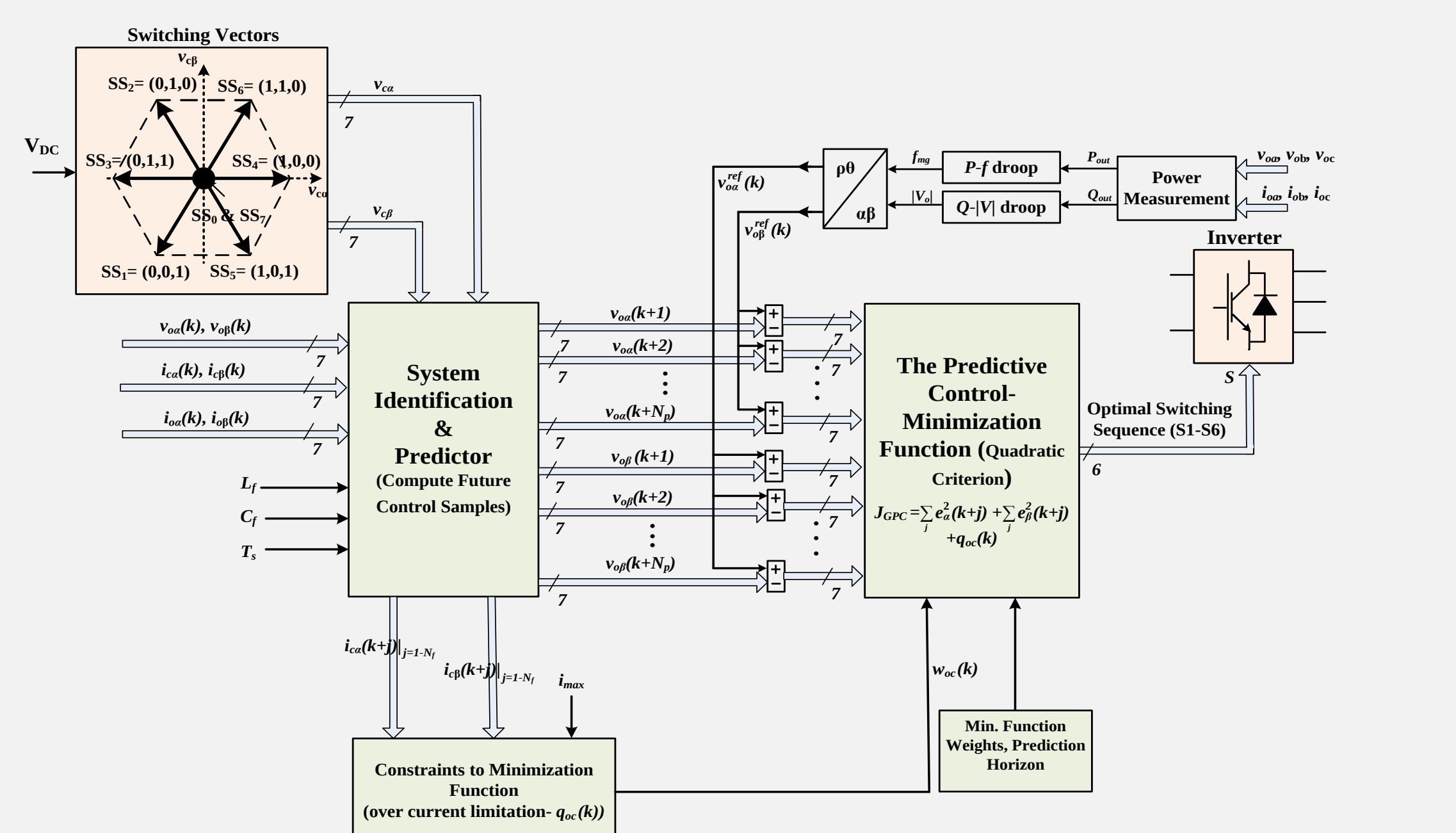
$$\mathbf{v}_o(k+N_p) = \mathbf{A}(z^{-1})\mathbf{v}_o(k) + \mathbf{N}_p \mathbf{B}(z^{-1})\mathbf{v}_c(k) + \mathbf{H}(z^{-1}) \left[\sum_{j=1}^{N_p} \mathbf{i}_c(k+j-1) \right] + \mathbf{N}_p \mathbf{G}(z^{-1})\mathbf{i}_o(k)$$

$$M_{GPC} = \sum_{j=1}^{N_p} e_a^2(k+j) + \sum_{j=1}^{N_p} e_b^2(k+j) + q_{oc}(k)$$

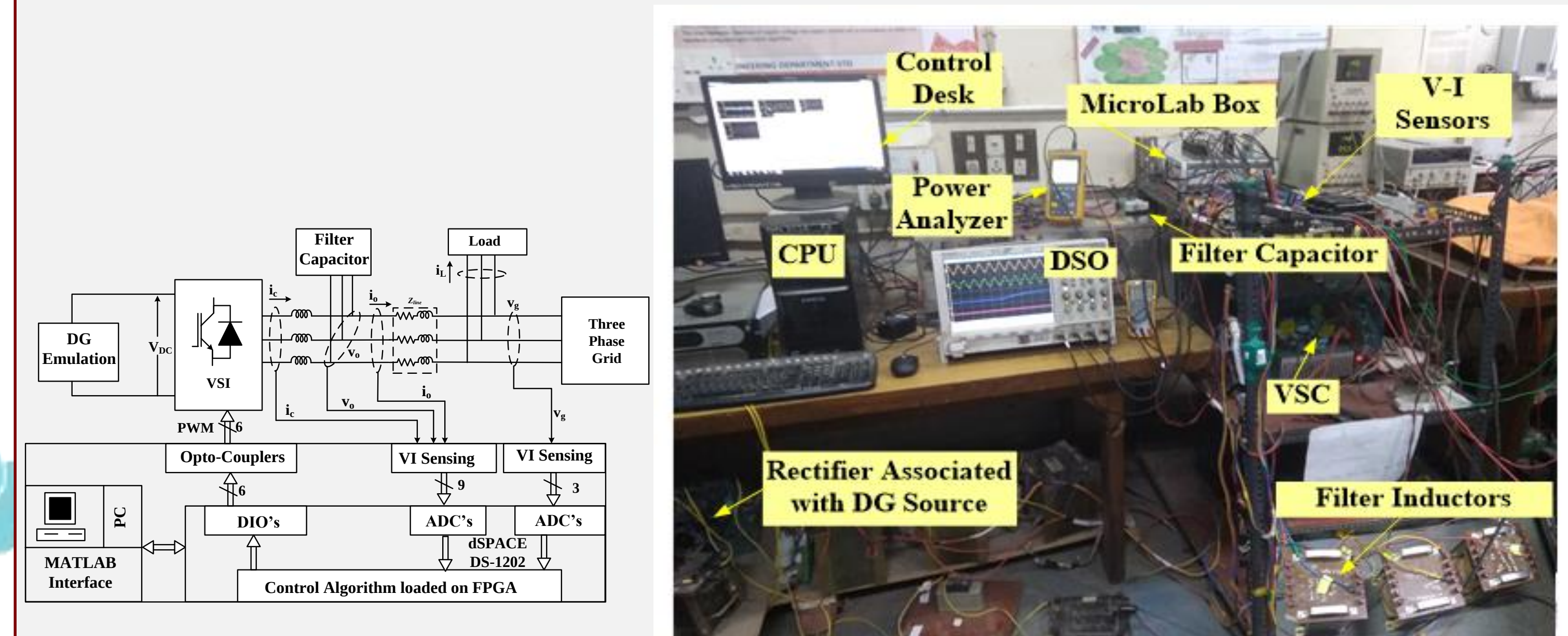
$$\mathbf{v}_c = \frac{2V_{DC}}{3} (S_a + S_b \alpha + S_c \alpha^2)$$

Where, $q_{oc}(k) = \begin{cases} 0 & \text{if } |i_{ca}(k+j)|_{\text{for } j=1 \text{ to } N_i} < i_{\max} \text{ and } |i_{cb}(k+j)|_{\text{for } j=1 \text{ to } N_i} < i_{\max} \\ \infty & \text{Otherwise} \end{cases}$

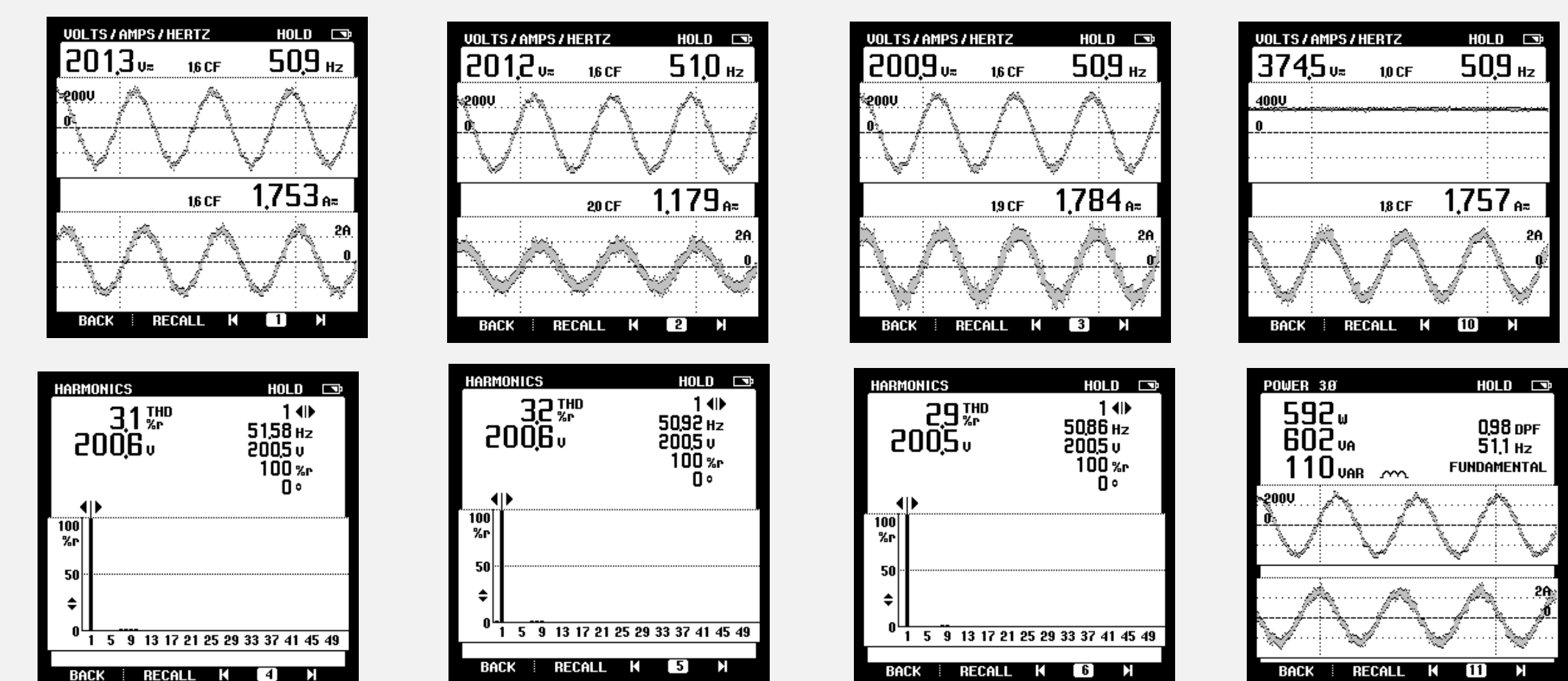
$$M_{GPC} = \sum_{j=1}^{N_p} e_a^2(k+j) + \sum_{j=1}^{N_p} e_b^2(k+j) + q_{oc}(k) + \mu \left[\sum_{j=a,b,c} |T_j(k+1) - T_j(k)| \right]$$



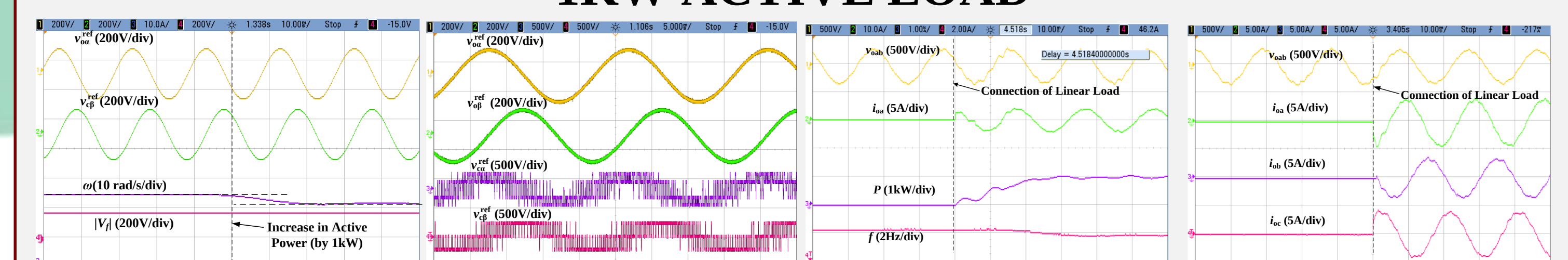
EXPERIMENTAL PROTOTYPE



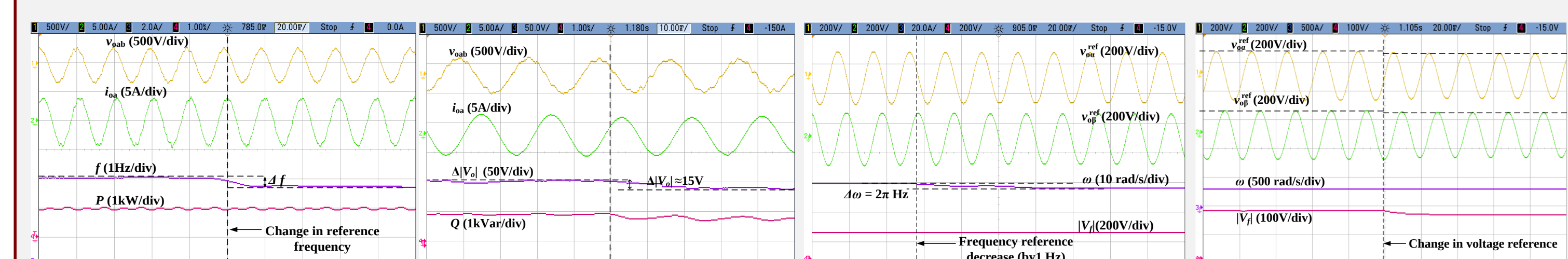
SYSTEM PERFORMANCE UNDER STEADY STATE CONDITION



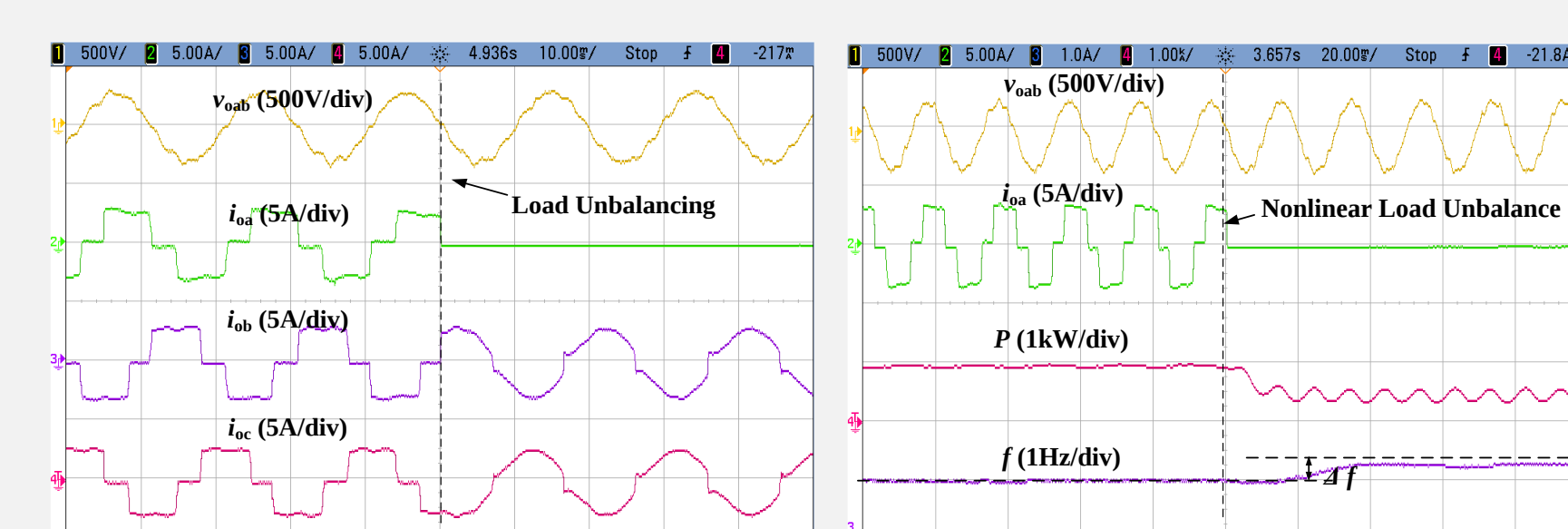
SYSTEM PERFORMANCE UNDER CONNECTION OF 1KW ACTIVE LOAD



SYSTEM'S RESPONSE TO THE CHANGES IN CONTROLLER REFERENCE INPUTS



SYSTEM'S RESPONSE TO THE CHANGES IN SYSTEM LOADS



CONCLUSIONS

The controller proves advantageous over the standard PWM controllers as it completely eliminates the PID regulators, PWM modulators and synchronous co-ordinate transformations. Thereby, the controller is easy for practical implementation purposes without any requirement for rigorous tuning.

The controller estimates the future samples of filter output voltages using the system model. The difference between the reference and estimated voltages, is nullified using a minimization criterion. The over-current protection is inherited within the control structure. Additionally, a switching frequency constraint is helps control the switching losses of the inverter. Simulation and experimental results are presented to verify the effectiveness of the proposed controller in tracking the reference voltages produced by the droop controller.

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